

Notice of References Cited	Application/Control No. 10/734,457		Applicant(s)/Patent Under Reexamination JOHNSON ET AL.	
	Examiner Qing Chen		Art Unit 2191	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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*	B	US-5,809,450	09-1998	Chrysos et al.	702/186
*	C	US-5,812,811	09-1998	Dubey et al.	712/216
*	D	US-6,212,544 B1	04-2001	Borkenhagen et al.	718/103
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Boothe et al., "Improved Multithreading Techniques for Hiding Communication Latency in Multiprocessors," 1992, ACM, pg. 214-223.
	V	Mowry, Todd C., "Tolerating Latency in Multiprocessors through Compiler-Inserted Prefetching," 1998, ACM, pg. 55-92.
	W	Xu et al., "Dynamic Instrumentation of Threaded Applications," 1999, ACM, pg. 49-59.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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